

Fig.1

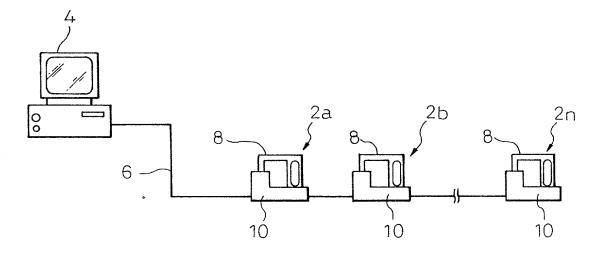
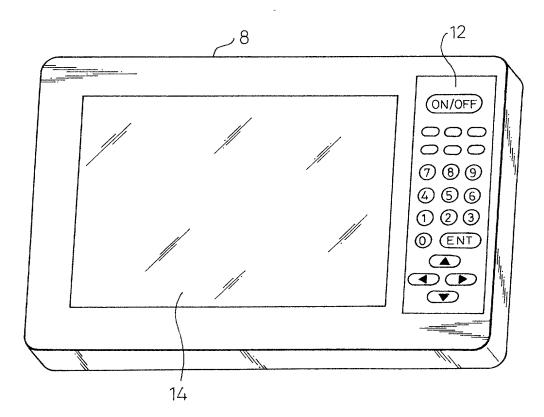
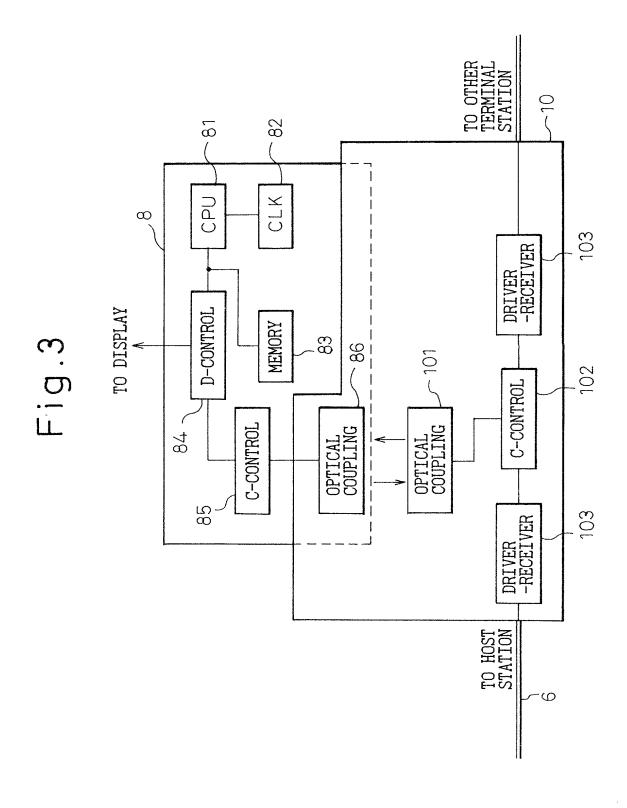


Fig.2





3/9 Fig.4

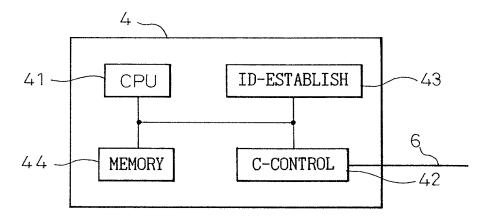


Fig.5

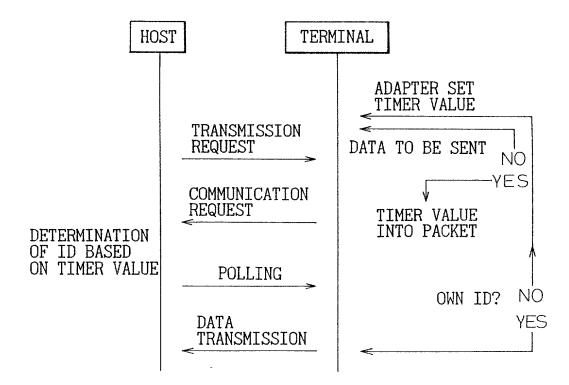


Fig.6

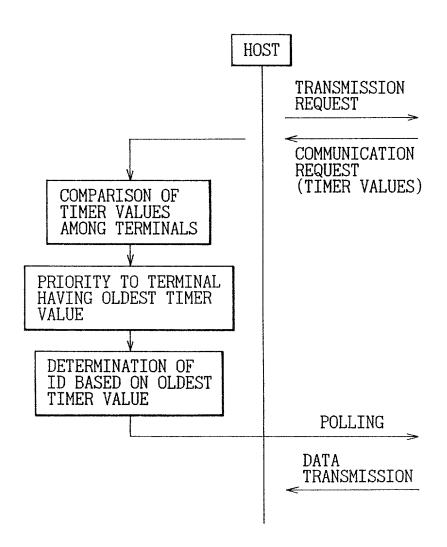
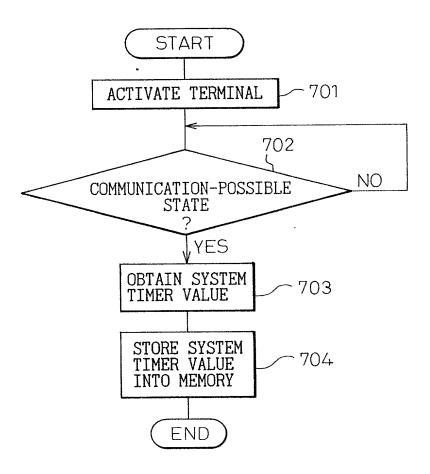
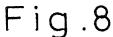


Fig.7



6/9



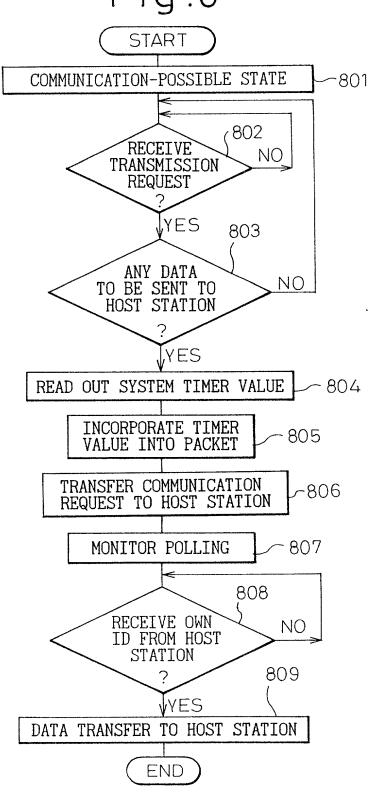


Fig.9

91 S	92	93	94	95
START BIT	SYSTEM TIMER VALUE	DATA	PARITY BITS	STOP BIT

Fig.10

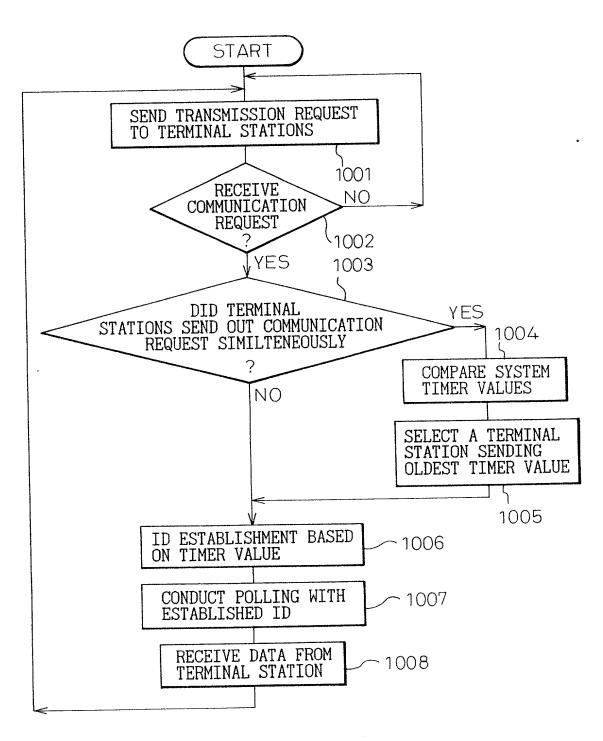


Fig.11

TERMINAL STATION	ID (TIMER VALUE)		
2a	00, ××, ∇∇		
2b	∆∆, ×ׄ oo		
2c	00, ×ׄ 00		
1 1	1 1 1		
2n	OO' ××" ××		